

FIG. 1

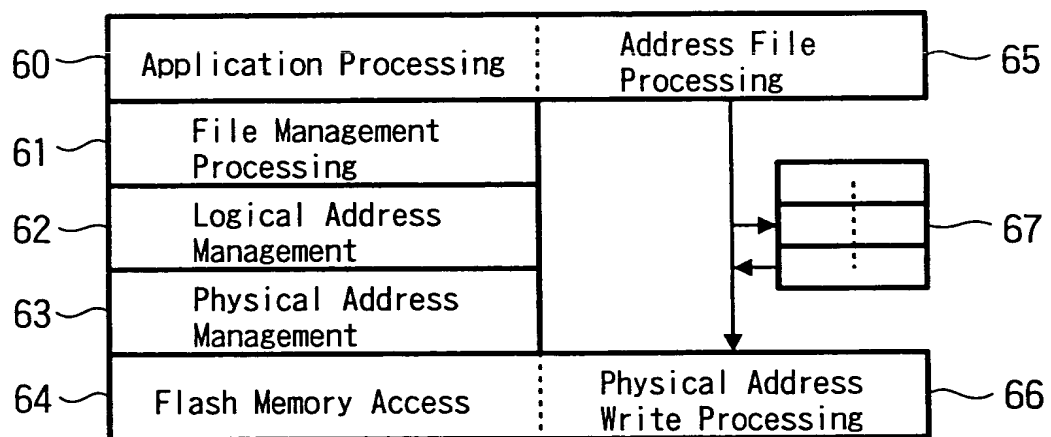


FIG. 2

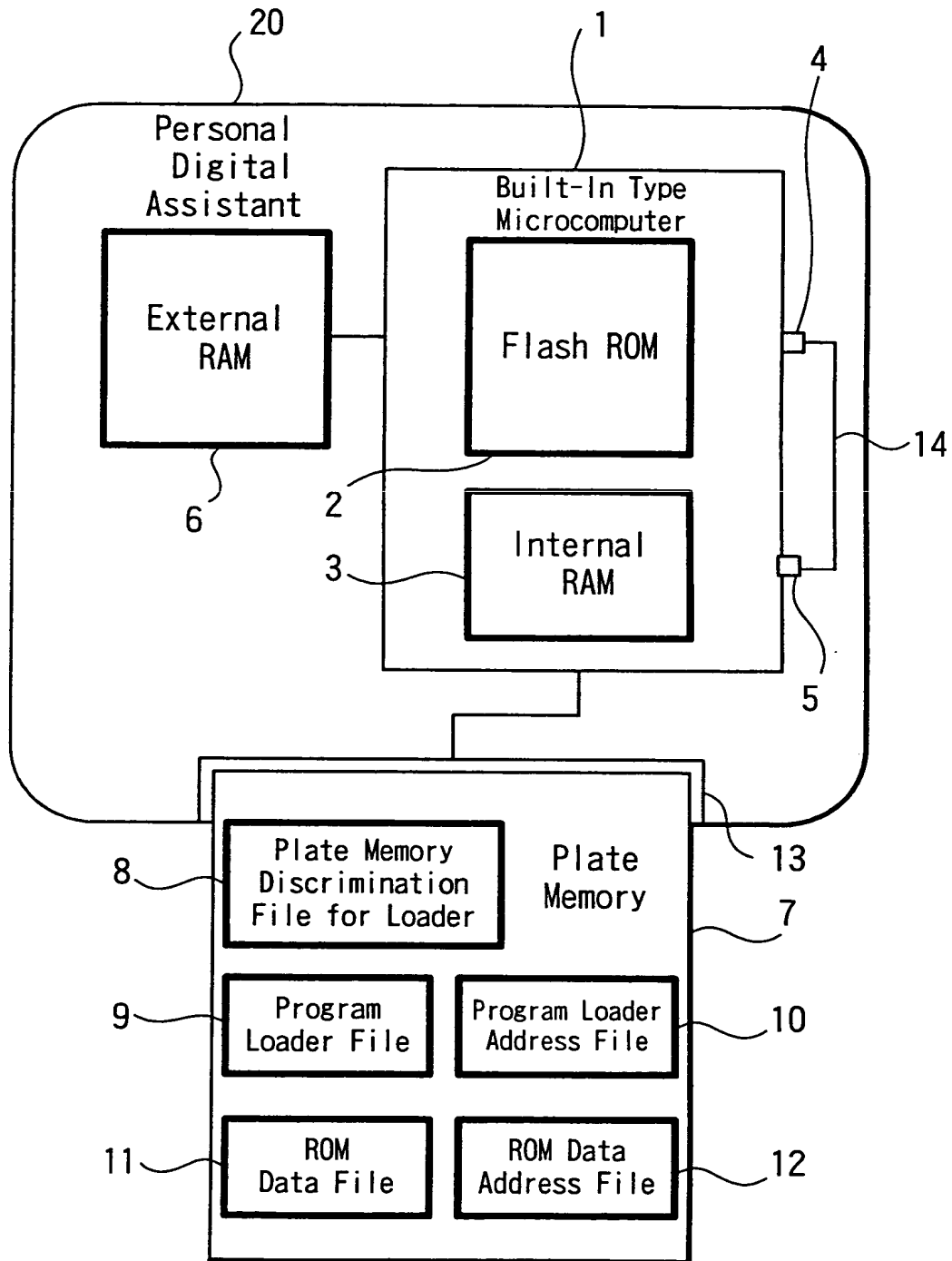


FIG. 3

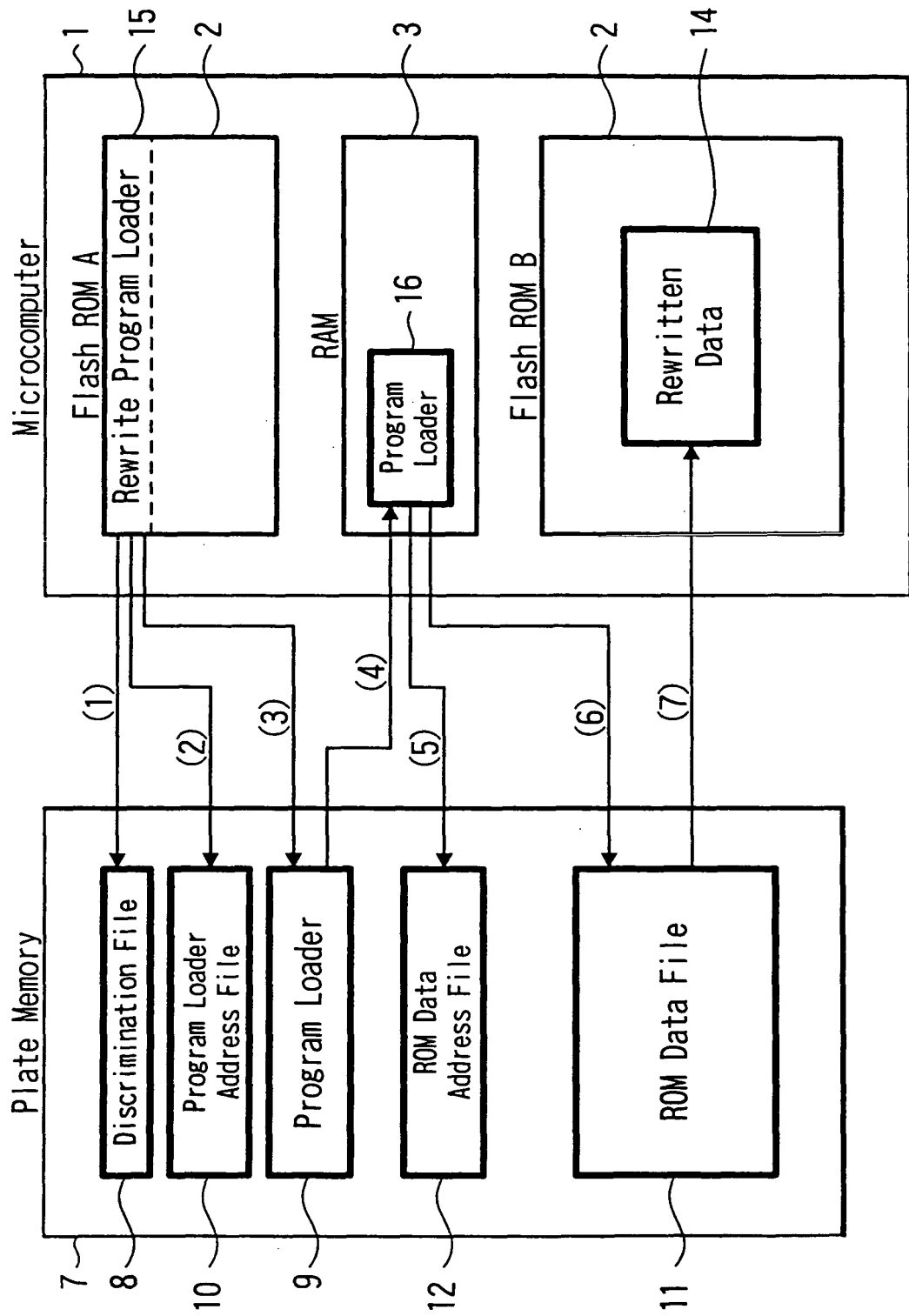


FIG. 4

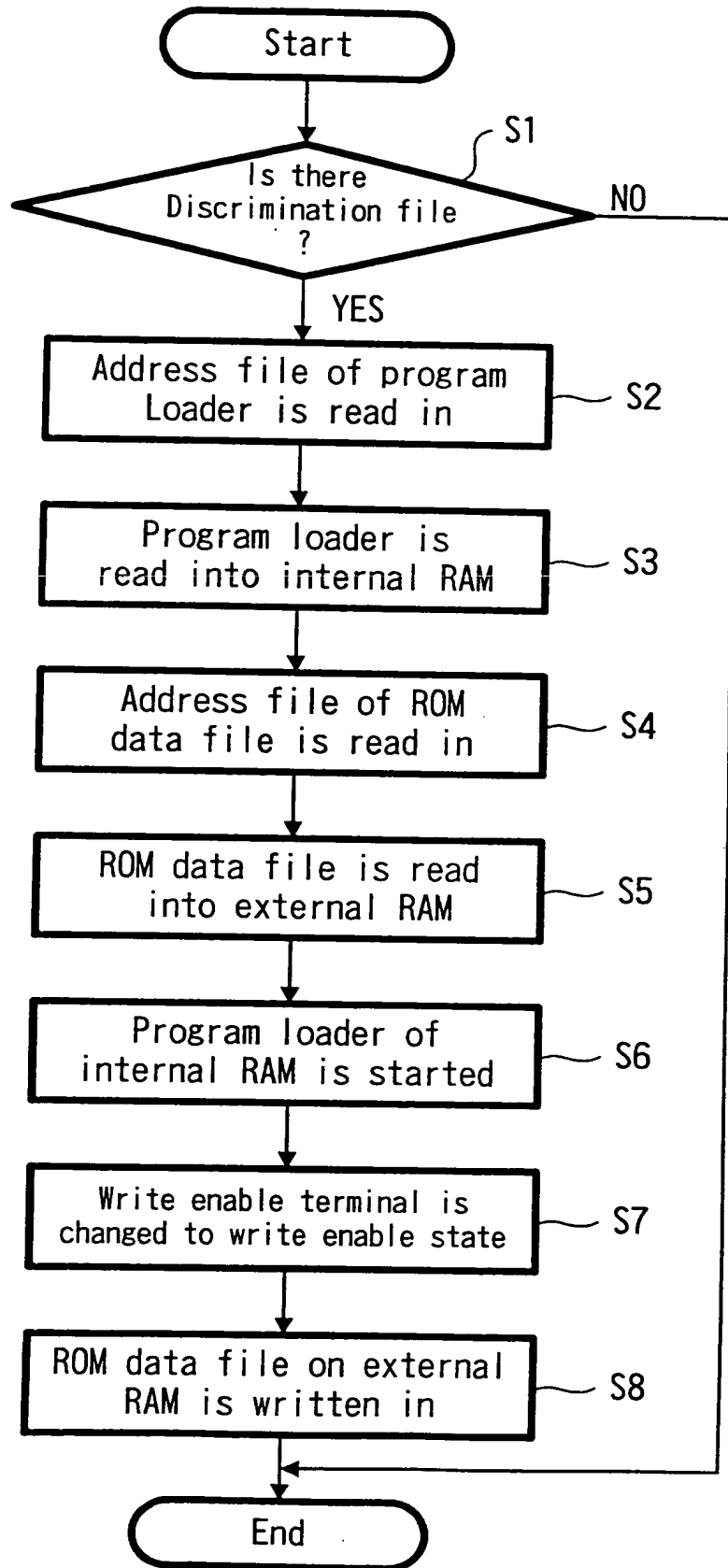


FIG. 5

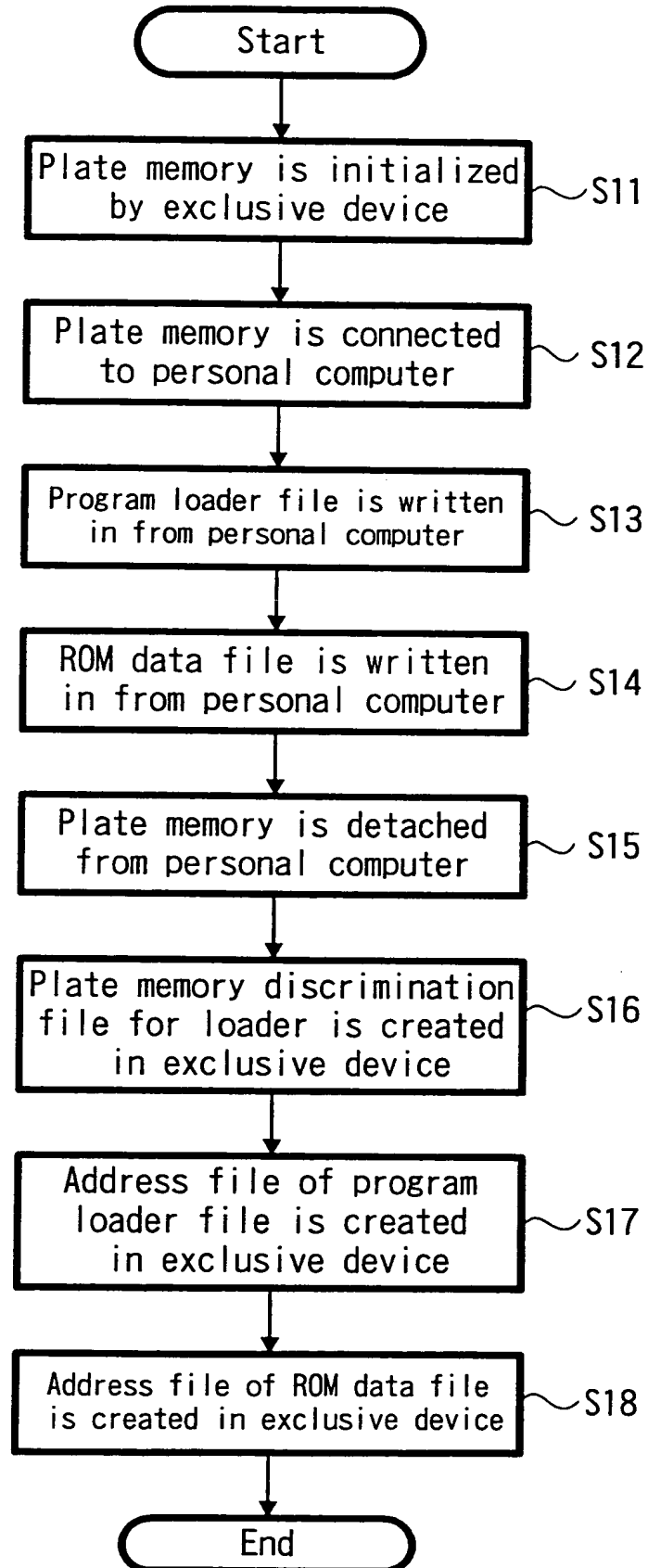


FIG. 6

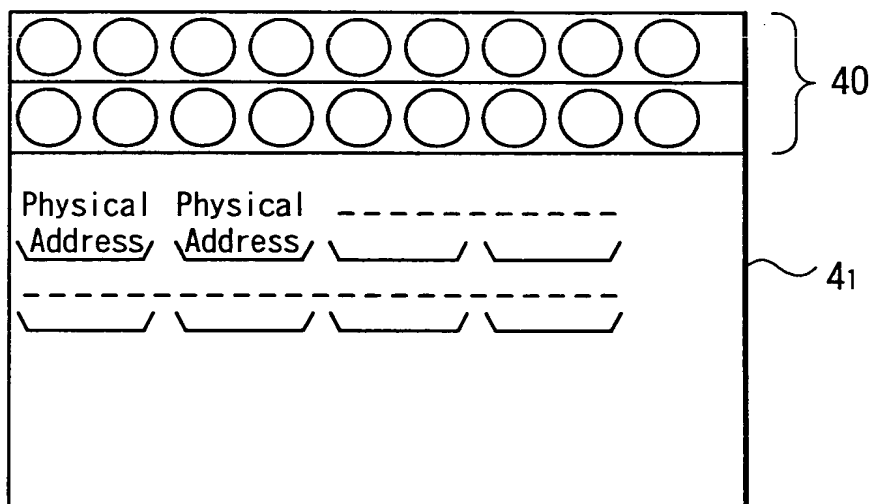


FIG. 7A

When loading from personal computer

	0×00	0×01	0×02	0×03	0×04	0×05	0×06	0×07	0×08	0×09	0×0A	0×0B	0×0C	0×0D	0×0E	0×0F
0×0000	A	B	C		D	E	F		M	E	M	S	T	I	C	K
0×0010		P	R	O	G	R	A	M		L	O	A	D	E	R	
0×0020	L	O	A	D		F	R	O	M		P	C			3	2

FIG. 7B

When loading from plate memory

	0x00	0x01	0x02	0x03	0x04	0x05	0x06	0x07	0x08	0x09	0x0A	0x0B	0x0C	0x0D	0x0E	0x0F
0x0000	A	B	C		D	E	F		M	E	M	S	T	I	C	K
0x0010		P	R	O	G	R	A	M		L	O	A	D	E	R	
0x0020	L	O	A	D		F	R	O	M		M	S			1	6

FIG. 7C

[illegible]

FIG. 7D

[illegible]

FIG. 8

Physical Block
Address

